

CONIELECOMP 2012

February 27 - 29, 2012

Auditorium UDLAP



Universidad de las Américas Puebla

UDLAP®

Opening Plenary



- **“Design of Analog Integrated Circuits in Submicrometer Technologies”.**

Jaime Ramírez Angulo.

Klipsch Distinguished Professor, IEEE Fellow, and
Director of the Mixed-Signal VLSI Lab at the Klipsch
School of Electrical and Computer Engineering

New Mexico State University. USA.

Plenary Session



- **“The Role of Interactive Digital Resources in Mathematics Education and Research”**

José Luis Abreu León.

founding member of the CIMAT (Centre for research in mathematics) in Guanajuato Mexico, researcher and director of the IIMAS (Applied Mathematics and Systems Research Institute) at UNAM

Laboratory of Innovation in
Educational Technology,
LITE. Mexico.

Plenary Session



- **“Probabilistic Reasoning in Answer Set Programming -- the P-log Approach”**

Michael Gelfond

He contributed in the implementation of a practical industrial-sized application of ASP-based planning for NASA on the Reaction Control System of the Space Shuttle

Texas Tech University. USA.

Plenary Session



- **“Service Robots”**

Luis Enrique Sucar

Member of the National Research System, the Mexican Science Academy, and Senior Member of the IEEE. He has served as president of the Mexican AI Society, has been member of the Advisory Board of IJCAI, and is Associate Editor of the journals *Computación y Sistemas* and *Revista Iberoamericana de Inteligencia Artificial*

Instituto Nacional de Astrofísica,
Óptica y Electrónica, INAOE.
Mexico.

Tutorials

- **“Some Techniques for Low Voltage Operation of Continuous-Time Analog CMOS Circuits”**

Jaime Ramírez Angulo.

New Mexico State University. USA.

- **“FPGAs”**

Juan Antonio Arízaga

Universidad Politécnica de Puebla. México

Tutorials

- **“Redes Automotrices”**

Eduardo López Sánchez

Universidad de las Américas Puebla. Mexico

- **“Simulink”**

David Báez López

Universidad de las Américas Puebla. Mexico

Tutorials

- **“ASP based architecture for intelligent agents”**

Michael Gelfond

Texas Tech University. USA.

- **“Semantic Technologies”**

Héctor Pérez Urbina, Clark-Parsia. USA.

Mauricio Osorio, UDLAP. Mexico.

María Auxilio Medina, UPP. Mexico.

Ofelia Cervantes, UDLAP. Mexico.

Final Program

- 70 papers selected from 118 submissions, through a rigorous review process. Technical Program Committee worked with 88 reviewers worldwide
- 4 plenary lectures
- 6 tutorial courses
- 1 book presentation, a Springer Edition
- Gala Dinner

Final Program

- “The Professor Muhammad Rashid Award” for CONIELECOMP’s Best Paper.

This year, the Best Paper will be awarded a diploma and a 300 USD prize

Registration Rates

	Before February 20, 2012		After February 20, 2012	
	Member IEEE	Non-member IEEE	Member IEEE	Non-member IEEE
Students	\$ 350.00	\$ 400.00	\$ 450.00	\$ 500.00
Non-students	\$ 800.00	\$ 900.00	\$ 900.00	\$1,000.00
Proceedings in CD	\$ 800.00	\$ 900.00	\$ 900.00	\$1,000.00

Current rate: Mexican Pesos

Registration site

- Laboratory of Electronics, UDLAP.
Since February 3, 2012

- Juan Antonio Navarro Martínez
Office: 2050-M

- 1st Semester Students don't pay to attend the Conference!!

However, Gala Dinner has a cost, please contact Juan Antonio Navarro Martínez for more information

juan.navarro@udlap.mx

Office: 2050-M

Tel: 2 29 26 81

CONIELECOMP 2012 Organizing Committee

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expo
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Gracias!

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